Design and Simulation of Compact, High Capacitance Ratio RF MEMS Switches using High-K Dielectric Material

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Abstract

In this paper, RF MEMS Capacitive Switches for two different dielectrics hafnium oxide (HfO₂) and silicon nitride (Si₃N₄) are presented. The switches have been characterized and compared in terms of RF performance. The major impact of the change from Si₃N₄ to HfO₂ having dielectric constant 20 is the reduction in overall dimension of the switch; capacitive area is reduced by 66% leading to overall reduction of about 41%. For 50nm thick HfO₂, the HfO₂ switches systematically shows an improvement in the isolation by more than -8dB (-35.16 versus -26.03 dB) and better insertion Loss at 12 GHz frequency compared to Si₃N₄. This makes HfO₂ an attractive dielectric for RF MEMS switch for new generation of low-loss high – linearity microwave switches.

Keywords: RF Switch; Actuation voltage; Coplanar waveguide; Down-state capacitance; Insertion loss.

1. Introduction

RF MEMS switches have drawn a lot of attention in the recent years because of their superior characteristics such as low insertion loss, high isolation and negligible power consumption compared with semiconductor switches. Due to the excellent performance at microwave to mm- wave frequencies have found applications in wireless and satellite communication system. A variety of RF MEMS switches have been designed with various actuation mechanisms such as electrostatic, piezoelectric...
or electro-thermal to operate the switch between two states (ON and OFF). Electrostatic actuation is most commonly preferred because of its low power consumption and simple fabrication technology. In capacitive type switches, in order to achieve better RF characteristics, a large capacitance ratio is desirable. Different methodologies have been adopted in order to achieve high capacitance ratio such as [1] Large overall Area, [2] Higher Gap and [3] Dielectric material with high-dielectric constant. This results in increase in overall dimension of the devices leads to in-built stress related deformation. Reliability is another main issue in RF MEMS switches. Stiction is a major limiter in the reliability of RF MEMS Capacitive switches due to dielectric charging. Charge trapping in Si$_3$N$_4$ dielectric layer results in the stiction of the switches when switches are actuated between the two states, high dielectric field across the thin dielectric layer causes positive and negative charge to tunnel into the dielectric and become trapped which depends upon the polarity of bias voltage. To eliminate this problem, many different solutions have been proposed such as the use of dielectric free capacitive switches, leaky dielectric so that charges can be easily detrapped, the bipolar activation alternatively switching actuation voltage between positive and negative levels and the other suitable dielectric by replacing Si$_3$N$_4$. This paper presents RF MEMS Capacitive Switch based on HfO$_2$ dielectric which results in reduction in overall dimension of the switch and results are compared with those for Si$_3$N$_4$ as a dielectric material.

2. Description of Switch
2.1 Device Concept
Three terminals RF Capacitive switch is based on 50Ω CPW configuration with movable metal beam made of conductive materials like Gold, Al etc. is mechanically anchored and electrically connected to the ground of CPW. The movable beam is suspended over a signal line as well as actuation electrodes at a gap of 2.5 µm on which a thin dielectric film is deposited. Two actuation electrodes introduced an additional degree of freedom. Figure [1] shows the working principle of switch. As shown in figure, when no bias voltage at the actuation electrode, the beam is at a 2.5 µm gap from the signal line; provides low insertion loss. Bias voltage applied at the actuation electrodes forces the beam to make a contact with signal line dielectric providing isolation in OFF- state. Equation 1 describe the pull-in voltage in terms of geometrical dimension of movable beam

$$V_p = \sqrt{\frac{8 K_z g^3}{27 e_o A}}$$  \hspace{1cm} (1)

Where g is the gap, A is the overall area and $K_z$ is the spring constant of beam respectively.
2.2 High-K Dielectric Material
Different types of high-k dielectric materials are available that could replace Si$_3$N$_4$ are tantalum oxide, hafnium oxide, barium strontium oxide, Al$_2$O$_3$ etc. Many of these materials are thermodynamically unstable on silicon and doesn’t meet other properties such low leakage current, high dielectric breakdown etc. Hafnium Oxide is one of the best materials in high- k dielectric because of its high dielectric constant and good process compatibility with concurrent IC technology. It also shows better resistance to dielectric charging, low leakage current and can be deposited as a thin layer down to 45nm. So hafnium oxide is suitable for various applications. The design parameters of RF MEMS Capacitive switch are shown in table 1.

<table>
<thead>
<tr>
<th>Dimension of Switch</th>
<th>Reference Switch</th>
<th>Optimized Switch</th>
</tr>
</thead>
<tbody>
<tr>
<td>Beam Thickness</td>
<td>1.5µm</td>
<td>1.5µm</td>
</tr>
<tr>
<td>Beam Length</td>
<td>500 µm</td>
<td>425 µm</td>
</tr>
<tr>
<td>Beam Width</td>
<td>100 µm</td>
<td>58µm</td>
</tr>
<tr>
<td>Gap Height</td>
<td>2.5 µm</td>
<td>2.5µm</td>
</tr>
<tr>
<td>Area</td>
<td>100×100 µm²</td>
<td>58×58 µm²</td>
</tr>
<tr>
<td>Electrode Thickness</td>
<td>1 µm</td>
<td>1 µm</td>
</tr>
<tr>
<td>Oxide Thickness</td>
<td>1 µm</td>
<td>1 µm</td>
</tr>
<tr>
<td>Dielectric Thickness</td>
<td>0.1 µm</td>
<td>0.05 µm</td>
</tr>
<tr>
<td>Secondary Meander Length</td>
<td>100 µm</td>
<td>100 µm</td>
</tr>
<tr>
<td>Primary Meander Length</td>
<td>30 µm</td>
<td>20 µm</td>
</tr>
<tr>
<td>CPW</td>
<td>60/100/60</td>
<td>40/58/40</td>
</tr>
<tr>
<td>Young’s Modulus of Gold</td>
<td>79GPa</td>
<td>79GPa</td>
</tr>
<tr>
<td>Poisson’s ratio of Gold</td>
<td>0.44</td>
<td>0.44</td>
</tr>
<tr>
<td>Dielectric constant</td>
<td>7.6(Si3N4)</td>
<td>20(HfO2)</td>
</tr>
</tbody>
</table>

Figure 1: RF Capacitive Switch
3. Simulation Results
3.1 DC Simulation
DC Simulations are done using CoventorWare software. 3D model of switch made using this software is shown in figure 1. Pull-in voltage, charge, capacitance, electrostatic force for switch is studied under DC simulation. Pull-in voltage comes out to be 4.8 to 5.2V for switch based on Si$_3$N$_4$ and for optimized switch based on HfO$_2$ as a dielectric. Mechanically optimized switch shows better response as compared Si$_3$N$_4$ based switches. Hysteresis loss is minimal in case of HfO$_2$ based optimized switches as shown in fig.2(a) as compared with Si$_3$N$_4$ based switches as shown in fig 2(b).

![Figure 2](image)

**Figure 2**: Comparison of Hysteresis Curve for Optimized HfO2 (a) and Si3N4 (b) based switches.

3.2 RF Simulation Comparison between HfO$_2$ and Si$_3$N$_4$
S-parameters measurements have been performed to compare the RF performance of the switches with different dielectric material using eq.(2) and (3). For equivalent dimension HfO$_2$ based switches shows better isolation and insertion loss at lower frequencies compared to Si$_3$N$_4$. Fig. 3(a) and 3(b) shows ON state and OFF state parameters of switch having same dimension but with different dielectric material. Isolation peak shifts to lower frequencies range with change in dielectric layer from Si$_3$N$_4$ to HfO$_2$ due to high capacitance of HfO$_2$. The advantage can be utilized by reducing the capacitive area to shift the operating frequencies in X-band. Using high – k dielectric materials results in reduction in overall dimension of the switch. Using HfO$_2$ as a dielectric in switch results in reduction of capacitive area of switch to almost 66% and this shows that the dimension of overall switch reduces upto 41 % of the dimension of Si$_3$N$_4$ based switches.
\[ |S_{11}| = \frac{\omega^2 C_z Z_0^2}{4} \]  

(2)

\[ |S_{21}| = \frac{4}{\omega^2 C_z Z_0^2} \]  

(3)

**Figure 3:** Comparison of (a) OFF state, (b) ON state response of switch with different dielectric materials for same dimension.

Fig. 4(a) and 4(b) shows the response of switch with Si3N4 as a dielectric and optimized switch with HfO2 as a dielectric in OFF and ON state respectively. Isolation in OFF and Insertion Loss in ON state is better in case of optimized switch with HfO2 as a dielectric as compare to Si3N4 based switch.

**Figure 4:** Comparison of (a) ON, (b) OFF state response of switch with Si3N4 and Optimized switch with HfO2 as dielectric.
4. Conclusion
The design optimization of switch has been presented by changing the dielectric from Si$_3$N$_4$ to HfO$_2$ for X-band (8-12GHz) applications. The shift in isolation curve due to change in dielectric layer has been studied. For X-band, the overall dimension of the switch can be reduced up to 41% while capacitive area reduction is 66% when changing dielectric to HfO$_2$ from Si$_3$N$_4$. Mechanical behavior of two configurations are also compared.

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References


